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TITLE:

MULTI-TASK PROCESSING SYSTEM

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INVENTOR - INFORMATION:

NAME

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APPL-NO:

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APPL-DATE:

December 16, 1988

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ABSTRACT:

PURPOSE: To increase the $\underline{\mathtt{task}\ \mathtt{switching}}$ speed by preparing plural pairs of

various register groups which are needed for execution of tasks of a CPU and

selecting and using an optional group via a group pointer register which

secures the correspondence to the tasks to be executed.

CONSTITUTION: At switch of tasks to be executed, an operating system 9

designates a group <u>pointer</u> to select and use various register groups 12

corresponding to the tasks to be executed and sets the group pointer to a group

pointer register 11. A CPU 10 selects the groups 12 based on the
pointer set

to the register 11 and executes the tasks in accordance with the contents of

the groups 12. As a result, the <u>task switching</u> speed is increased.



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